1

5

6 14

18

19

20

20

20

Table of Contents — Section Two

SOLID STATE CONTROL FUNDAMENTALS

Comparing basic control systems Logic equivalents Glossary of symbols

CONVERSION TO SOLID STATE

Relay diagrams to solid state diagrams Word descriptions to solid state diagrams Boolean equations to solid state diagrams

DEVELOPING A BILL OF MATERIAL

Solid state modules
Mounting racks
Power supply
Hardware and accessories

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G.	h - 8	n e
E .		

Solid state control fundamentals

Basic control systems

All control systems share three common features: Inputs, logic functions and outputs. (Figure 2-1).

Figure 2-1a illustrates a common machine control diagram. The inputs are items such as limit switches and pushbuttons. The outputs are solenoids, motor starters or lamps. The logic for the system is provided by the sequencing of the machine.

Figure 2-1b shows the conventional relay control approach. The inputs are still such things as limit switches and pushbuttons. The outputs are still such things as solenoids, motor starters or lamps. The logic functions are performed by hard-wired relay contacts.

The solid state system shown in Figure 2-1c, however, is quite different. For one thing, the inputs and outputs are in two parts external and solid state. The external elements are still the same limit switches, pushbuttons, solenoids, motor starters and lamps. These, however, operate at voltages that are too high for the solid state logic devices and, in addition, often operate on AC while the solid state logic requires DC. It is obvious, therefore, that conversion is needed from high voltage AC to low voltage DC on the input side, and from low voltage DC to high voltage AC on the output side.

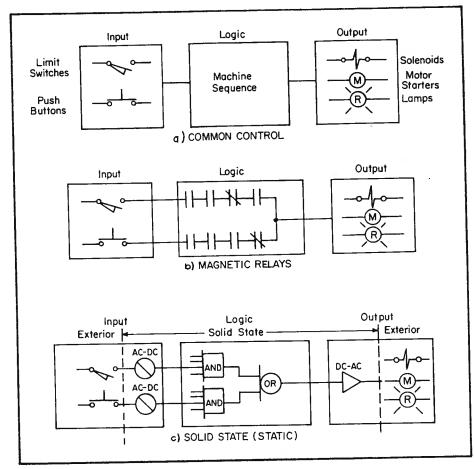


Figure 2-1: Block diagrams of basic control systems.

Logic equivalents

In order to work with solid state controls, one needs to understand the tools available... the functional building blocks. A good way to accomplish this is by first examining the relay control functions and then relate them to the solid state functions.

AND Function. The relay circuit shown in Figure 2-2 is a simple series contact circuit which represents the AND logic function. Contacts A AND B AND C AND D must be closed to energize relay coil E, closing contact E and opening contact E (NOT E). Or, putting it another way, the four contacts must be closed to provide an output at E and not an output at E. Note that a normally-open contact is designated by a plain letter, while a normally-closed contact is designated by a letter with a line above it.

The equivalent solid state AND logic function operates similarly; when signals are present at inputs A AND B AND C AND D. an output is generated at E and the normal output at \overline{E} is cancelled. Should any one or more of these inputs be removed, the AND gate will revert to its normal state of no output at E and an output at \overline{E} .

OR Function. Figure 2-3 shows a basic parallel relay contact circuit, representing the OR function. With the relay circuit, closing contact A OR B OR C OR D energizes coil E, closing contact E and opening contact E. Opening all contacts deenergizes coil E, opening contact E and closing contact E.

Again, the solid state OR function operates in the same manner; closing switches A OR B OR C OR D, singly or in any combination, results in an output at E and no output at E. Opening all switches cancels the output at E and reactivates the output at

It should be noted with this or any other logic function that has two outputs (E and E), both

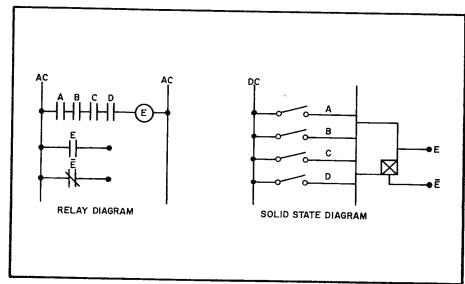


Figure 2-2. The AND function

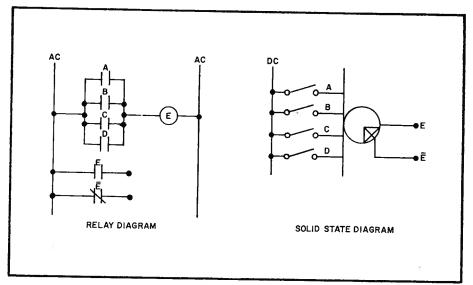


Figure 2-3. The OR function

outputs do not have to be used. In fact, a logic function normally is considered as having only one output (E). The NOT output (E) is added to 300 Series devices to provide greater convenience and flexibility. In actual practice, either or both outputs can be used depending upon the needs of the circuit.

TIME DELAY Functions. This function can be used either as an ON-DELAY or an OFF-DE-LAY. Using the ON-DELAY relay circuit in Figure 2-4, the coil TD-E is first energized and then, after a selected time delay, contact E closes and contact E opens. When coil TD-E is de-energized, contact E opens immediately and contact E closes. The OFF-DELAY relay circuit operates just the reverse; energizing coil TD-E immediately closes contact E and opens contact E. Then, when the coil is deenergized, there is a time delay before contact E opens and contact E closes to their normal state.

Another way to designate the state of any logic input or output is to use "1" for on, and "0" for off. Thus, for the ON-DELAY logic function in Figure 2-4, when input A is operating at logic 1, a selected time delay will pass, and output E will be at logic 1 and E will be at logic 0. Then, when the input switches to a logic 0, output E immediately changes to logic 0 and output E changes to logic 1.

Likewise, with the OFF-DELAY function, when the input switches to logic 1, output E immediately changes to logic 1 and E changes to logic 0.

When the input is removed at logic 0, there will be a time delay before E changes to logic 0 and E changes to logic 1.

RETENTIVE MEMORY Functions. Certain control applications require that the result of an input be retained after the input is removed. This is illustrated by the relay circuit in Figure 2-5. Energizing coil E latches the memory, closing contact E and opening contact E. Thus, when

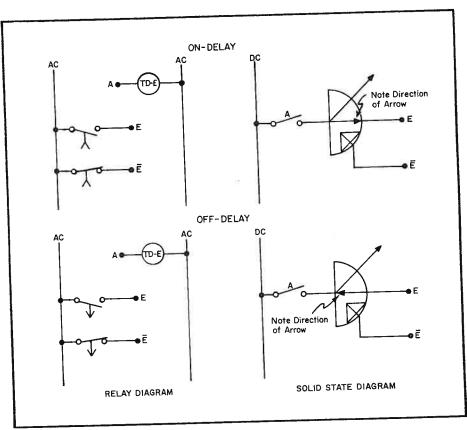


Figure 2-4: The TIME DELAY functions.

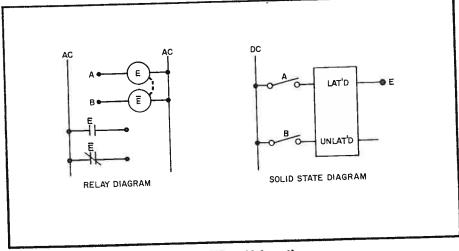


Figure 2-5: The RETENTIVE MEMORY function.

coil E is de-energized, the two contacts remain in their set positions. To return the contacts to their normal state, coil E is energized, unlatching the mem-

ory and reversing the state of the contacts. Should power be interrupted at any time, the output contacts will remain in their existing state.

The solid state RETENTIVE MEMORY operates in much the same way. Momentary closing of switch A results in a continuing logic 1 output at E. Momentary closing of switch B returns output E to its normal logic 0 state and produces a logic 1 output at E. As with its relay counterpart, the existing output state at power interruption is retained.

Special Functions

Some control systems require functions which can be performed more economically with special devices. Such devices include: FLIP-FLOPS, PARALLEL and SERIAL INPUT SHIFT REGISTERS, TIMERS and ONE SHOTS. These special devices are described in Section 6 on the individual device specification sheets.

AC INPUT. Solid state logic devices operate on low voltage DC. Therefore, any external high voltage AC inputs must be converted to low voltage DC and conditioned to remove contact bounce and electrical noise before they can be fed into the solid state system. The symbol for such AC INPUT devices is shown in Figure 2-6.

Closing AC limit switch LS results in a DC logic 1 output at E. Conversely, opening switch LS results in a DC logic 1 output at E.

AC OUTPUT. In a similar manner, the low voltage DC outputs of the solid state system must be converted to high voltage AC to activate the external devices. This is accomplished by AC OUTPUT devices, whose symbol is shown in Figure 2-7.

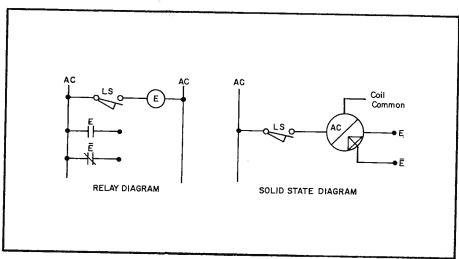


Figure 2-6: AC INPUT.

Energizing coil E closes contact E which energizes the AC solenoid. Removing the AC input at A de-energizes coil E which deenergizes the AC solenoid.

Closing DC switch A results in an AC output at E, energizing the AC solenoid. Opening switch A results in no output at E, deenergizing the AC solenoid. A composite chart covering all the logic equivalents discussed above is included on the next page.

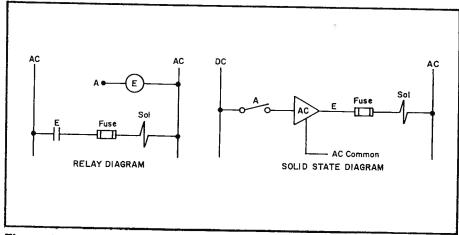
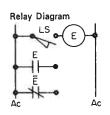


Figure 2-7: AC OUTPUT.

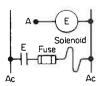
Glossary of Control Diagram Symbols



Relay Statement

Ac Input Closing LS energizes coil E, closing contact E and opening contact E

Opening LS de-energizes coil $\underline{\underline{E}}$ opening contact $\underline{\underline{E}}$ and closing contact $\underline{\underline{E}}$



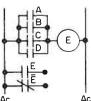
AC Output Energizing coil E closes contact E and energizes the Solenoid



Series Contacts

Closing contact A AND contact B AND contact C AND contact D energizes coil E closing contact E and opening con-

Opening any one Contact or combination de-energizes coil E opening contact E and closing contact $\bar{\rm E}$



Parallel Contacts

Closing contact A OR contact B OR contact C OR contact D OR any combination of contacts energizes coil E closing contact E and opening

Opening all contacts de-energizes coil $\underline{\underline{E}}$ opening contact $\underline{\underline{E}}$ and closing contact $\underline{\underline{E}}$



On-Delay Timer

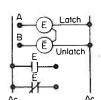
Energizing coil of timer TD-E causes contact E to time closed after a period of time, and closed contact E times

De-energizing Timer coil TD-E Instantaneously opens contact E and closes contact E



Off-Delay Timer Energizing coil of Off-Delay timer TD-E instantaneously closes contact E and opens contact E

De-energizing the coil Off-Delay timer TD-E causes contact E to time open and contact E to time closed after a time delay.



Retentive Memory Energizing coil E latches the memory closing contact E

Energizing coil Eunlatches the memory closing contact E

The existing output state at power interruption will be retained.



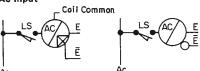
Ac Output

Dc

ANSI Y32.14

Amplifier

Ďс



Solenoid

Ac - 120 Volts - Ac AND Gate with NOT

(Inverted) Output

OR Gate with NOT

(Inverted) Output

Adjustable On-De-

(Inverted) Output

lay Timer with NOT

Adjustable Off-De-lay Timer with NOT

(Inverted) Output

English Logic Statement

Closing switch LS results in an output

Opening switch LS results in an output at E

IS=E

LS = E

Boolean Equation

Closing switch A results in an Ac out-put at E

Opening switch A results in no output

A = E

A = E (No Output)

Ac - 120 Volts - Ac Closing switches A AND B AND C AND AND Gate with **Inverted Output**

Solenoid

Ē

Fuse

D result in an output at E

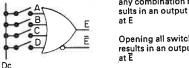
Opening any one switch or any combination results in an output at E

 $\vec{A} \cdot B \cdot C \cdot D \cdot = \vec{E}$

 $A \cdot B \cdot C \cdot D \cdot = E$

Closing switches A OR B OR C OR D or any combination re-

A+B+C+D=E



OR Gate with inverted Output

Opening all switches

 $\overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D} \cdot = \overline{E}$

results in an output at E

Adjustable On-Delay Timer with Inverted Output

ON-Delay

OFF-Delay

Closing switch A results in an output at E after a time delay

A=E (After Time Delay)

Opening switch A

 $\bar{A} = \bar{E}$

results in an instan-taneous output at E

Closing switch A results in instan-

A = E

taneous output at E Opening Switch A

results in an output at E after a time delay

A=E (After Time

Delay)

Momentary closing of switch A results in an output at E

Momentary closing of switch B results in an output at E, resetting E

B=E

The existing output state at power inter-ruption will be retained

Retentive Memory

Unlatch Dc

Flip Flop (Latch)

Adjustable Off-Delay Timer with

Inverted Output



S=Set R = Reset

Converting relay schematic diagrams to solid state diagrams

A relay schematic or ladder diagram is an excellent starting point for developing an equivalent solid state logic diagram. Although there are a number of methods to accomplish this, the "circle method" is one of the simplest and fastest. This method is explained through the following example:

Figure 2-8 is a pictorial representation of a drilling station on a simple machine. This example was chosen because such a drilling station is found on many machine tools, and the complexity of the circuit covers a large percentage of the relay diagrams you may encounter.

Start-up for the unit requires setting a selector switch to "automatic" or "manual" and energizing the spindle motor via a pushbutton.

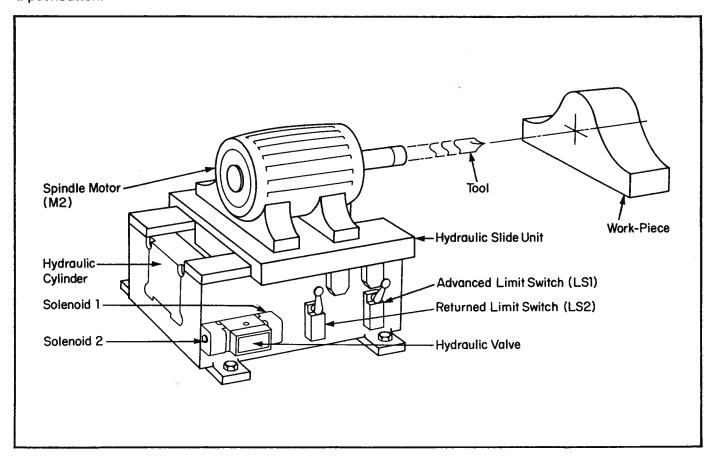
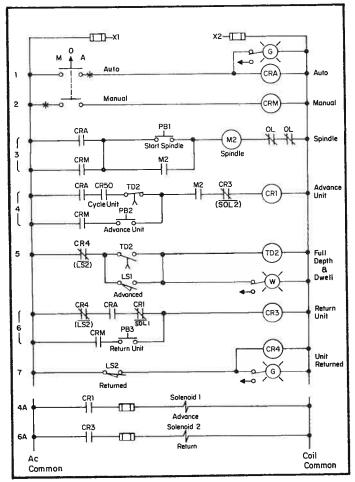


Figure 2-8: Typical machine tool drilling station.

When set for automatic operation, the unit cycles upon command from the central control (CR50) of the machine. First, the "advance" hydraulic control valve is energized by a solenoid, causing the unit to advance to the work piece. Second, when



X2-[]] 11 X Manual CRM Manual Start Spindle M2 CR3 (50L2) Cycle Unit Advanc CRI PB2 TD2 Full Depth CR4 5 LSI Dwell (W) Advanced 6 CR4 Unit Returned € 7 Returned Solenoid 1 Advance 6A Coil Ac Common Common

Figure 2-9: Original version of ladder diagram.

Figure 2-10: Revised version of ladder diagram.

the unit's full advanced position is reached, a limit switch is closed, energizing a dwell timer and a "full depth" indicator at the operator's station. Third, after a short time delay, the "advance" hydraulic valve is deenergized and the "return" valve is energized. Fourth, when the full return position of the unit is reached, a second limit switch is closed, de-energizing the "return" valve. All cycling controls are returned to normal and the unit is ready for the start of another cycle.

Figure 2-9 shows the ladder diagram for our example as it would be drawn normally. Since the circle method requires the grouping of series and parallel contacts, the diagram has been redrawn to make these circuits easier to recognize as shown in Figure 2-10. The line numbers on the left vertical bus provide the correlation between the two versions.

The first step in the conversion process is to select the "inputs". This is done by circling and numbering each input as shown in Figure 2-11. The primary concern is with identifying and counting the 120-volt AC pilot devices that are located outside

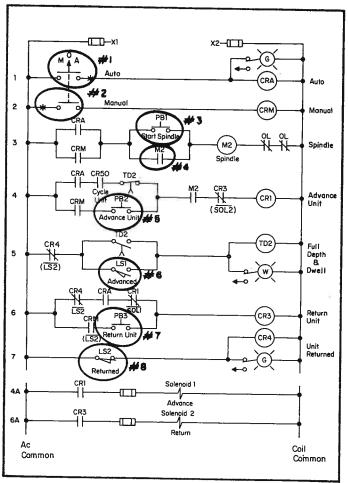


Figure 2-11: Selection of panel inputs.

the panel. The only exceptions to this are interlock contacts from other panels and the motor starter seal contacts within the panel. Generally, only one contact of each input pilot device need be wired to the solid state panel. The equivalents of the closed or open contacts are generated electronically in the AC input circuits by selection of the true or NOT function.

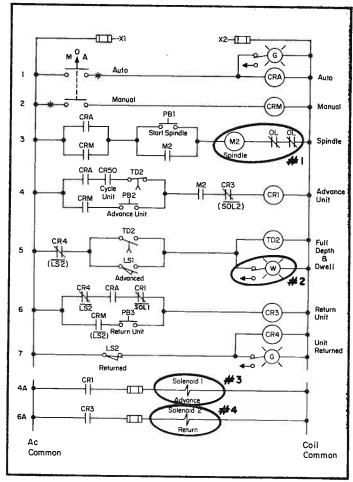


Figure 2-12: Selection of panel outputs.

The second step is to select the "outputs". Here again, circle and number them as shown in Figure 2-12. The only things to be concerned with are the machine outputs driven from relay contacts. Outputs driven directly from limit switches or pushbuttons, such as lamps, remain as in the relay schematic and do not require solid state AC outputs.